

Docket No. 212812US2
IN RE APPLICATION OF: Takashi IPPOSHI, et al.
SERIAL NO: 09/930,202
FILED: August 16, 2001
FOR: SEMICONDUCTOR WAFER



2815

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COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:
Transmitted herewith is an amendment in the above-identified application.

- ☒ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☒ Additional documents filed herewith: Marked-up Copy, Letter Submitting Replacement Drawing Sheets, and 3-Sheets of Drawings

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	10	MINUS	20	0	x \$18 =	\$0.00
INDEPENDENT	3	MINUS	3	0	x \$84 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$280 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☐ A check in the amount of \$0.00 is attached.
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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DOCKET NO.: 212812US2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
TAKASHI IPPOSHI, ET AL. : EXAMINER: NGUYEN, J.
SERIAL NO: 09/930,202 :
FILED: AUGUST 16, 2001 : GROUP ART UNIT: 2815
FOR: SEMICONDUCTOR WAFER :

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Official Action dated March 21, 2003, please amend the above-identified patent application as follows:

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IN THE CLAIMS

Please amend claims 5 and 8 as shown in the attached marked-up claims to read as follows:

5. (Amended) A semiconductor wafer comprising:
a first semiconductor wafer; and
a second semiconductor wafer having a crystal orientation display section to be a nick
indicative of a crystal orientation formed on a fringe,
wherein said first and second semiconductor wafers are bonded to each other such
that a part of a main surface of said first semiconductor wafer is exposed by said crystal
orientation display section of said second semiconductor wafer, and

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